

100

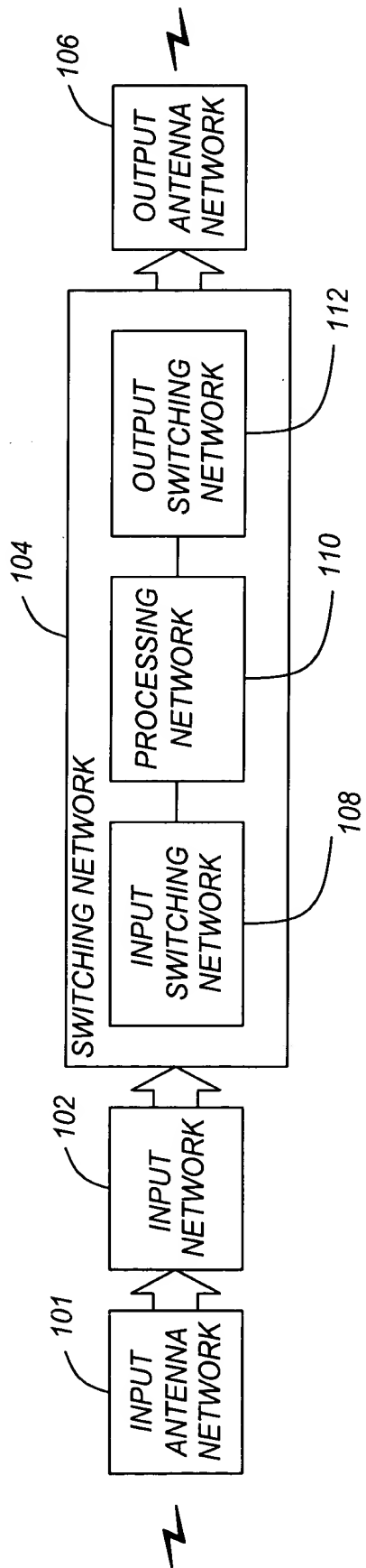


FIG. 1

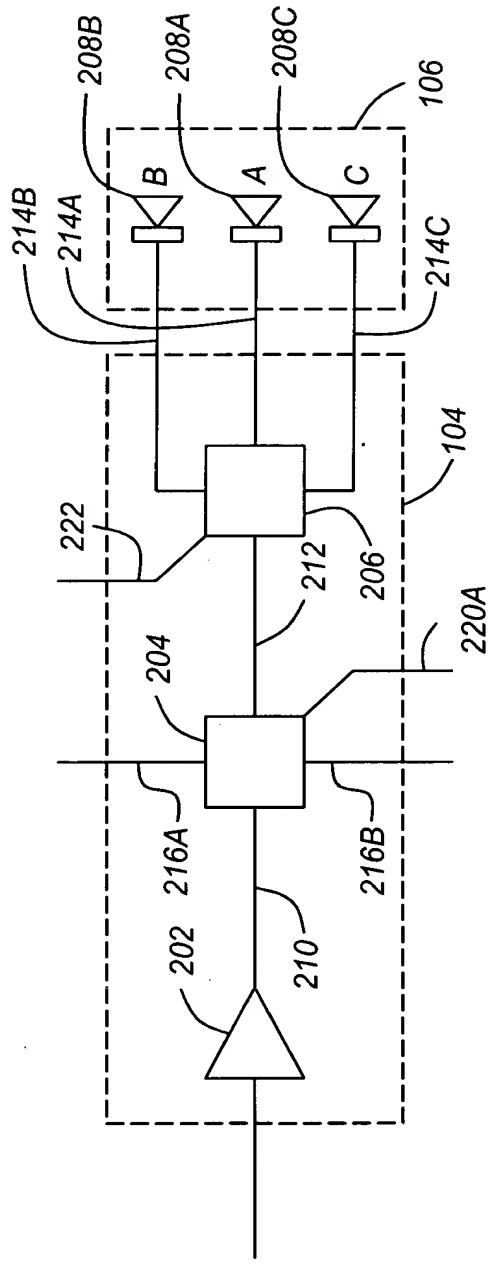


FIG. 2  
PRIOR ART

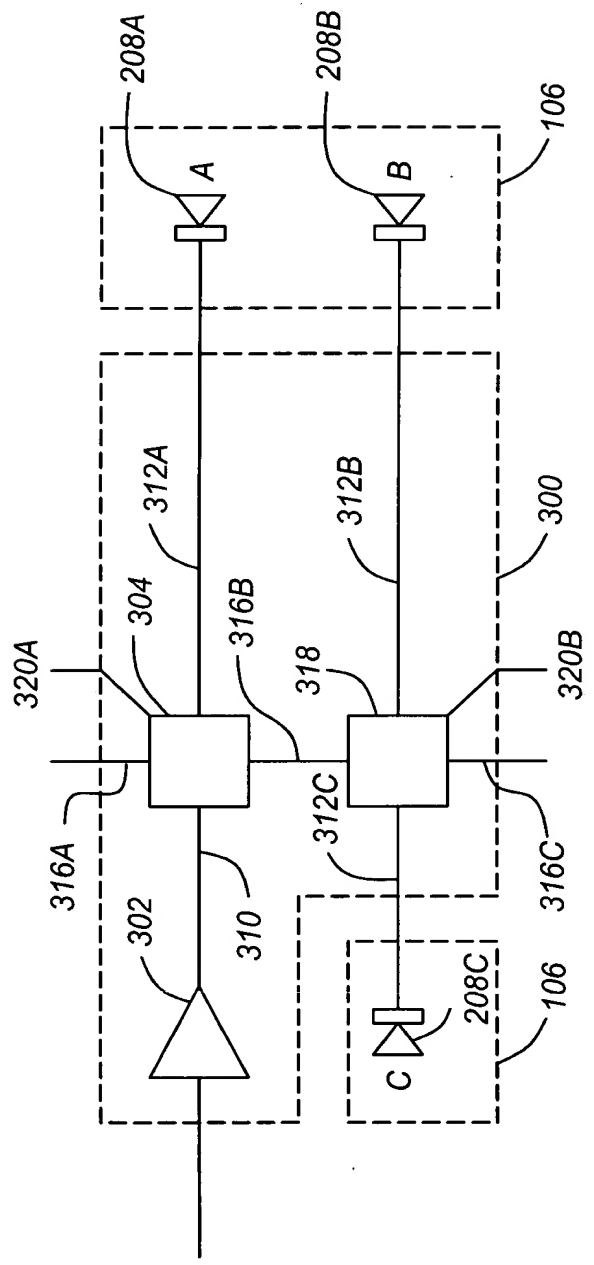


FIG. 3

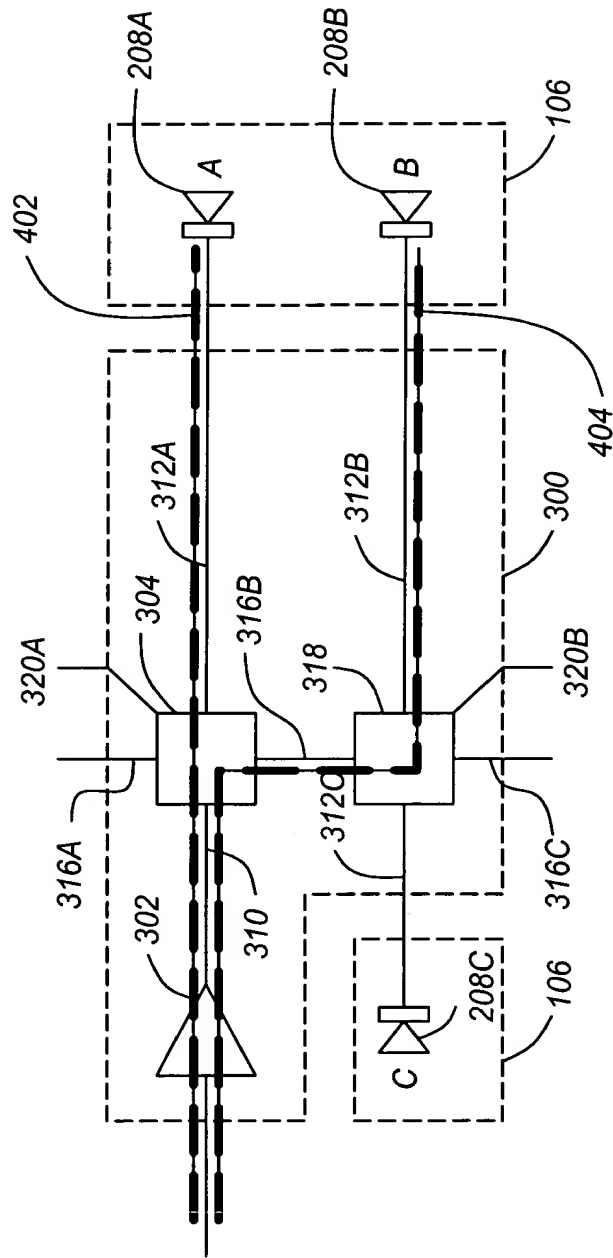


FIG. 4

FIG. 5 is a schematic diagram of a system 500.

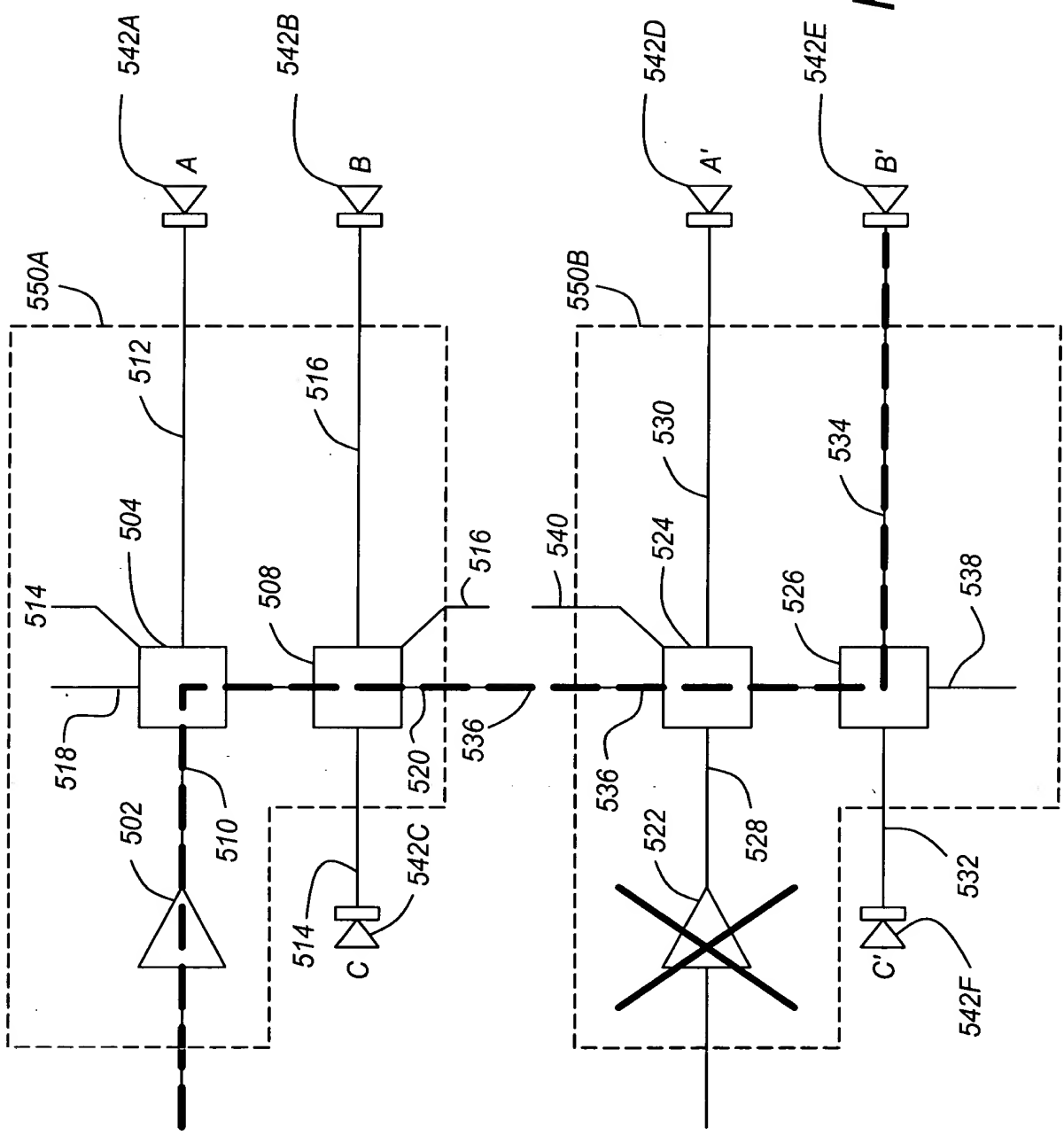
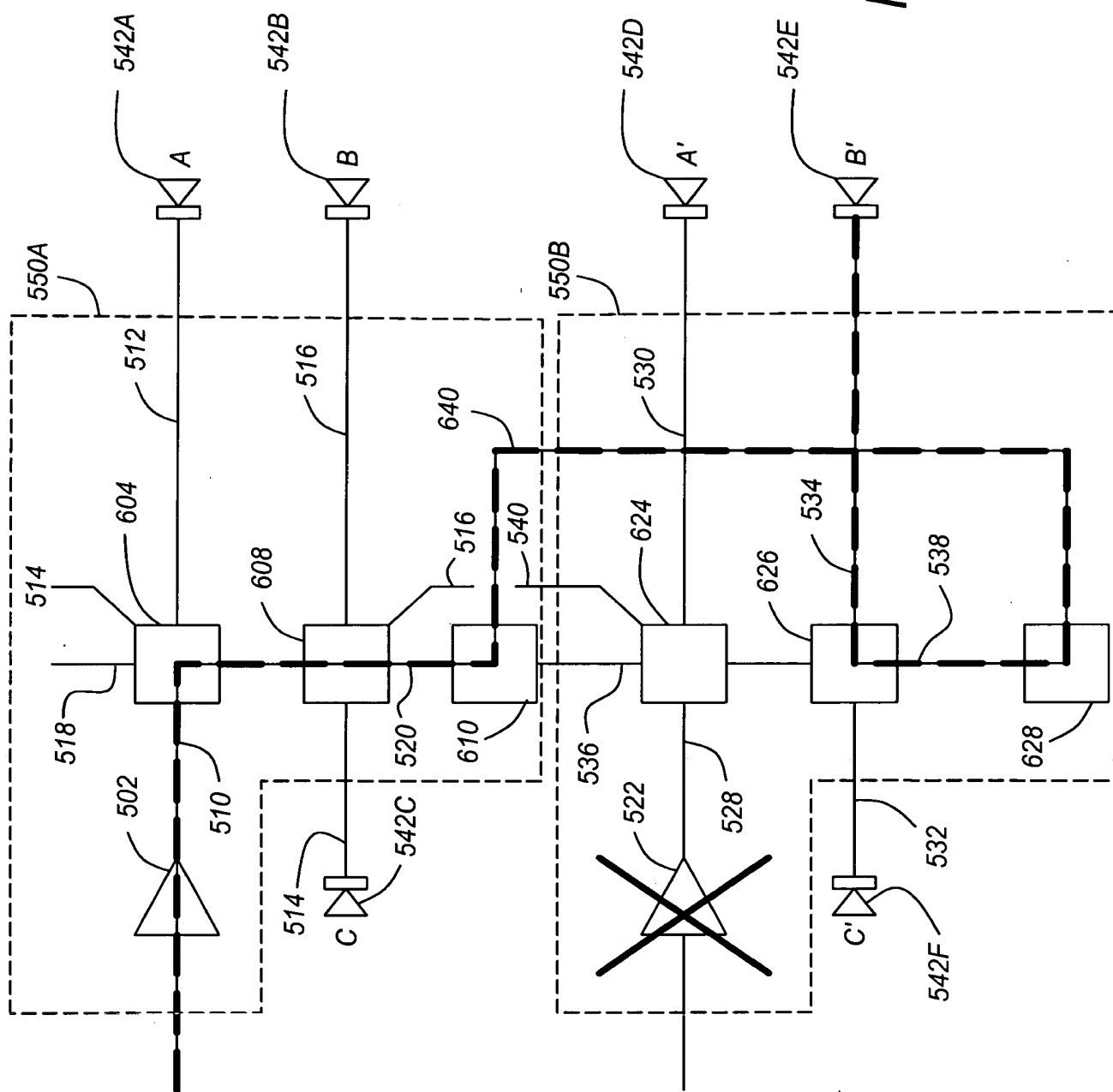


FIG. 5



**FIG. 6**

700

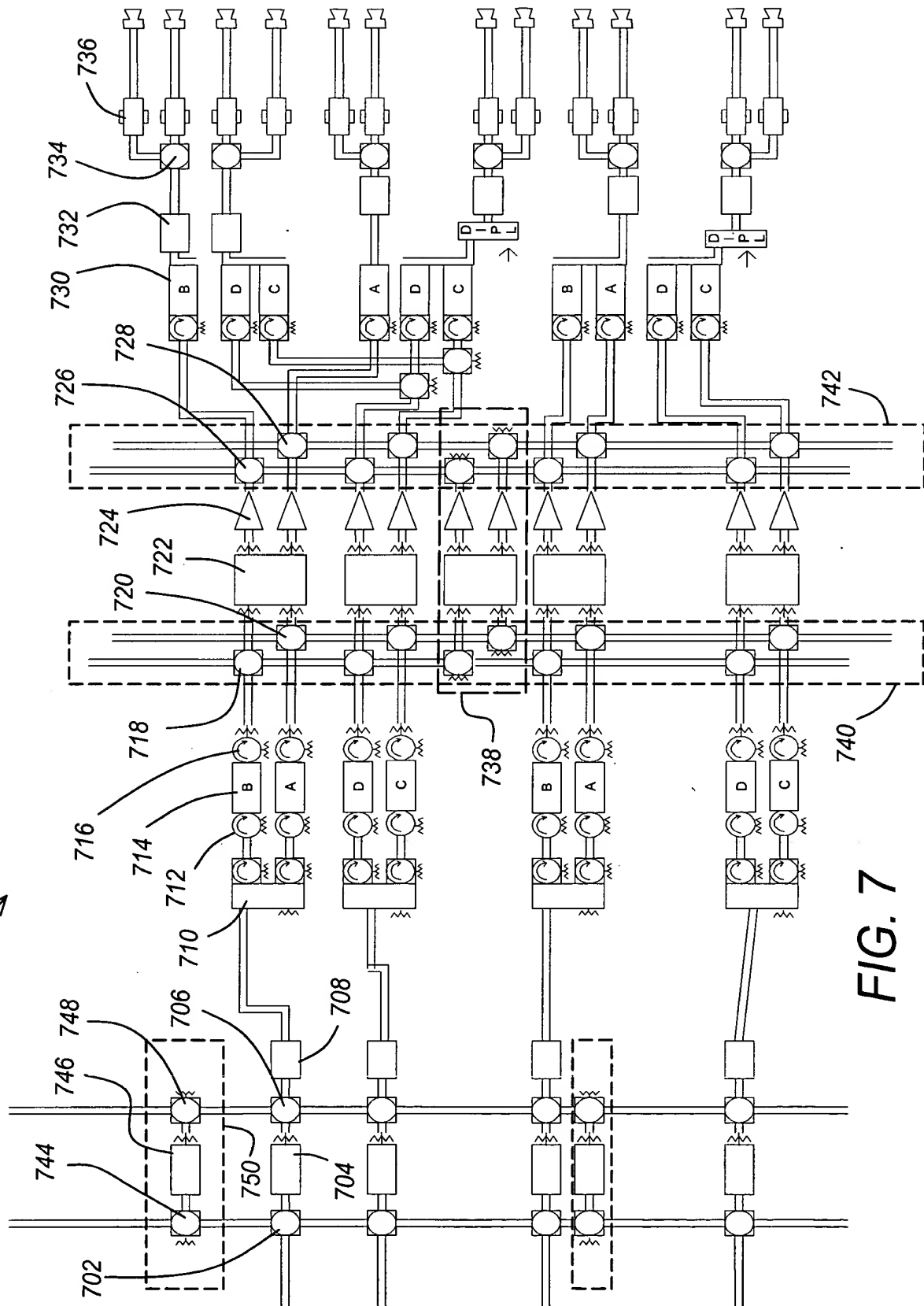


FIG. 7

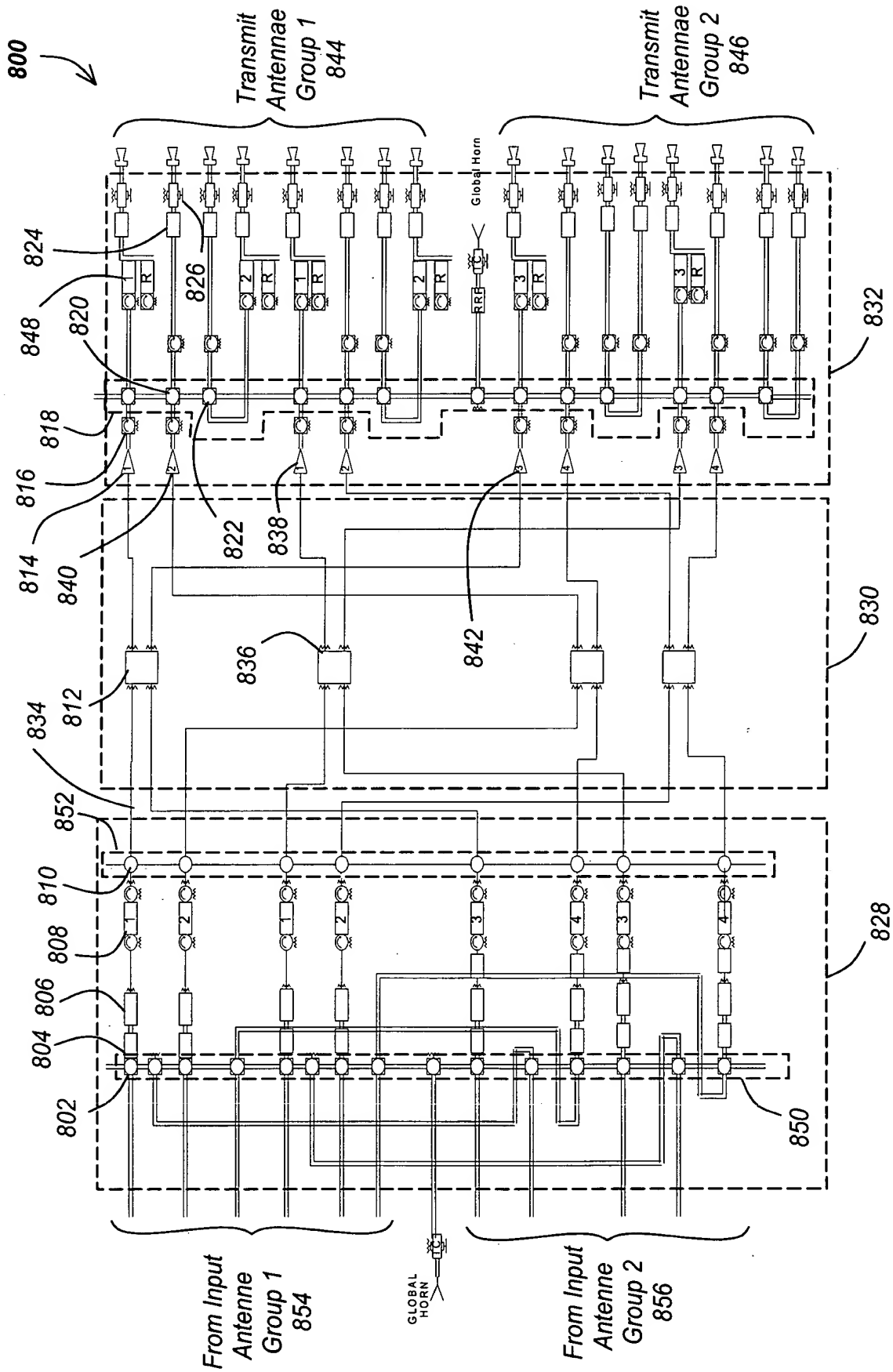


FIG. 8

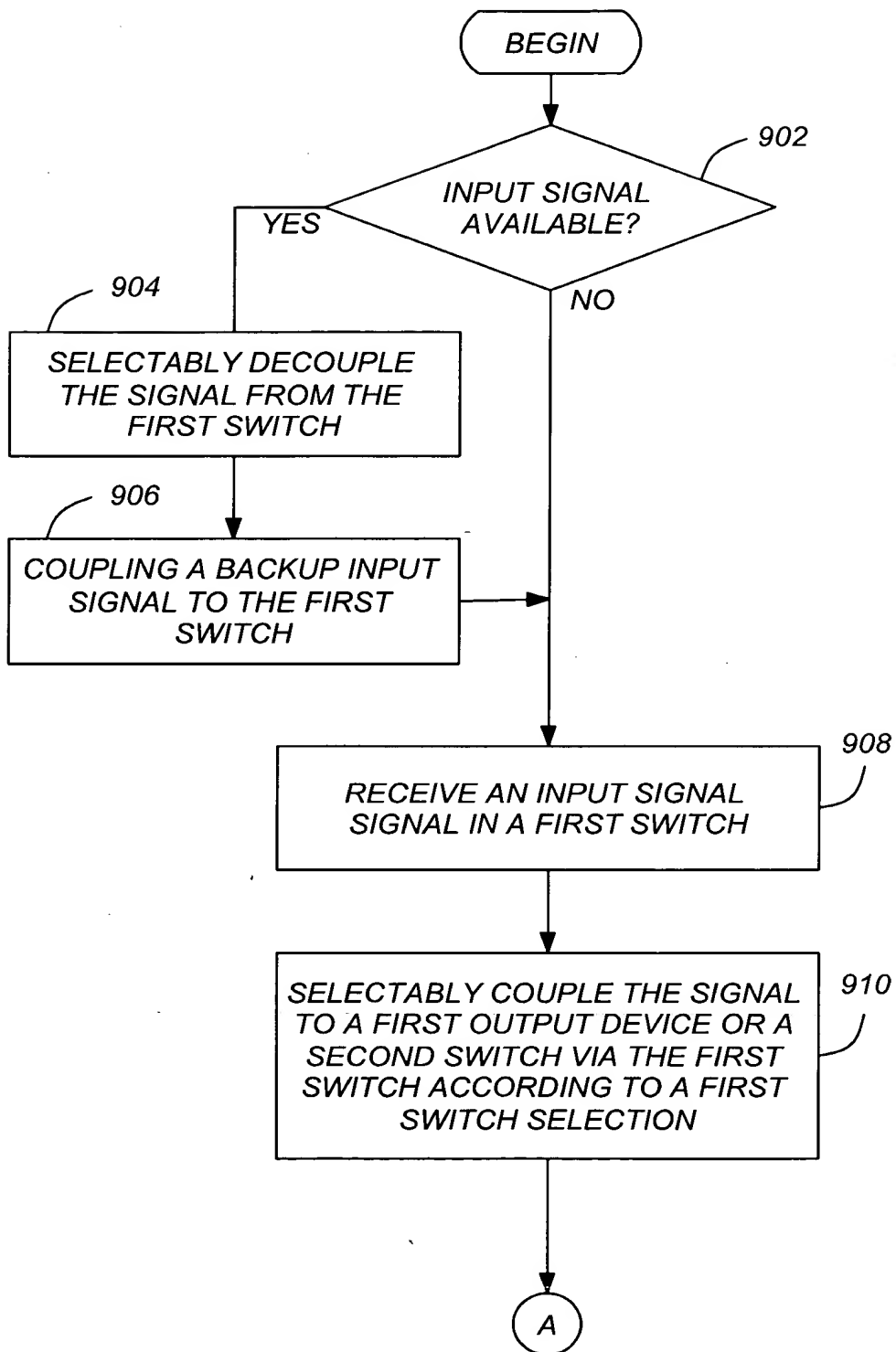


FIG. 9A



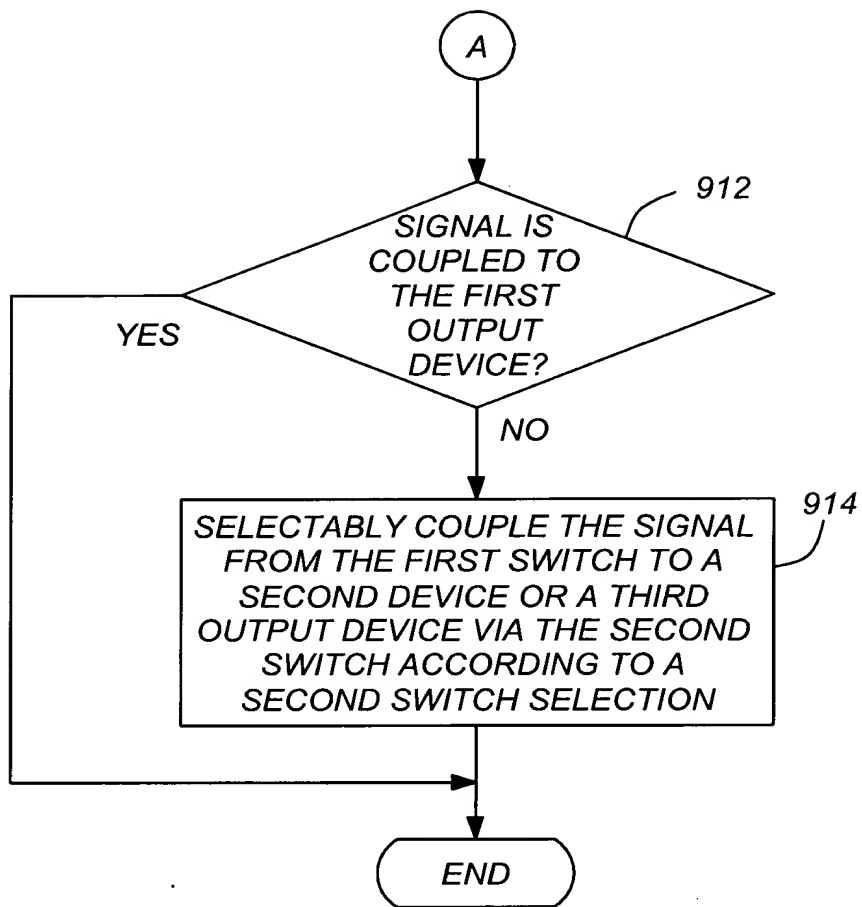


FIG. 9B